Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER- III (NEW) EXAMINATION - SUMMER 2022

Subj	ect	Code:3130704 Date:18-	07-2022
Subj	ect]	Name:Digital Fundamentals	. ,
Time	e:02	:30 PM TO 05:00 PM Total M:	arks:70
Instru			
		Attempt all questions.	
		Make suitable assumptions wherever necessary. Figures to the right indicate full marks.	
		Simple and non-programmable scientific calculators are allowed.	
			MARKS
Q.1	(a)	List out various logic families. Also list characteristics of digital IC.	03
	(b)	A 7	04
	(c)		07
		a) $F(A,B,C) = \sum (1,3,6)$	
		b) $F(A,B,C) = \pi(2,3,5)$	
Q.2	(a)	Perform the binary subtraction using 2's complement	03
		$(0111)_2 - (1101)_2$	
	()	Convert the decimal Number 250.5 to base 4 and base 8.	04
	(c)		07
	()	OR	0.7
	(c)	Explain various logic gates.	07
Q.3	(a)	Compare Half adder and Full adder.	03
Q.5	(b)		04
	(c)		07
	(-)	OR	
Q.3	(a)		03
		F(A, B, C, D)=ABC'D'+ABC'D+ABCD'+AB'CD'	
	(b)		04
	(c)	Explain Minterm and Maxterm.	07
0.4	(2)	Civis the difference between acquestial singuit and combinational	0.3
Q.4	(a)	Give the difference between sequential circuit and combinational circuit.	03
	(b)	Explain Look-ahead Carry generator.	04
	(c)		07
	(-)	OR	
Q.4	(a)	Explain NAND SR Latch.	03
	(b)	Explain clock triggering mechanism.	04
	(c)	What is race around condition (racing)? How to solve it?	07
	, ,		
Q.5	(a)	Classify different types of digital to analog converters.	03
	(b)		04
	(c)	List out different types of ROM. Also explain ROM. OR	07
Q.5	(a)		03
7.0	(b)		04
	(c)	Describe operation of D/A converter with binary-weighted resisters.	07
