GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-IV (NEW) EXAMINATION – WINTER 2021

| Subject Code:3141002 Date:31 | | 12/2021 | |
|--|------------|--|---------------|
| Sub | ject | Name: Analog Circuit Design | 7 |
| Time:10:30 AM TO 01:00 PM Total Marks: | | | larks: 70 |
| Instr | ruction | The state of the s | |
| | | Attempt all questions. | |
| | 2. | Make suitable assumptions wherever necessary. | |
| | | Figures to the right indicate full marks. Simple and non-programmable scientific calculators are allowed. | |
| | ••• | Simple and non-programmable belefitine carealators are anowed. | MARKS |
| Q.1 | (a) | Define following terms. | 03 |
| Q.1 | (a) | SVRR, Slew Rate, CMRR. | 05 |
| | (b) | List ideal characteristics of OP-AMP. | 04 |
| | (c) | Explain inverting integrator circuit using OP-AMP. | 07 |
| | | | |
| Q.2 | (a) | Derive gain expression for voltage shunt F/B amplifier using OP-AMP. | 03 |
| | (b) | Explain peak detector using OP-AMP. | 04 |
| | (c) | Write and explain differential amplifier using three OP-AMP. | 07 |
| | (-) | OR | 07 |
| | (c) | Explain Instrumentation amplifier circuit operation using OP-AMP. | 07 |
| Q.3 | (a) | Explain circuit made up of OP-AMP that does addition of inputs. | 03 |
| Q. 5 | (b) | Explain 1 st order low pass filter using OP-AMP. | 04 |
| | (c) | Explain 2 nd order high pass filter using OP-AMP with derivations. | 07 |
| | \ / | OR | |
| Q.3 | (a) | Define following terms. | 03 |
| | | Lock Range for PLL, Capture Range for PLL, Frequency Stability 1 | for |
| | | Oscillators. | |
| | (b) | Explain class A power amplifier. | 04 |
| | (c) | Draw and explain square wave generator using OP-AMP. | 07 |
| 0.4 | (a) | Explain grounded load V to I converter using OP-AMP. | 03 |
| Ų. 4 | (a) (b) | Explain grounded load v to reconverter using OF-AMF. Explain wien bridge oscillator using OP-AMP in detail. | 03 |
| | (c) | Explain CE short-circuit current gain without resistive load R _L . | 07 |
| | (0) | OR | |
| Q.4 | (a) | Explain voltage limiter circuit using OP-AMP with suitable example. | 03 |
| | (b) | Draw and explain class B push pull power amplifier. | 04 |
| | (c) | Derive expression for trans-conductance gm in Hybrid – Π model. | 07 |
| | / | | |
| Q.5 | (a) | Design Monstable multivibrator for $T_P = 1.1$ millisecond, take $C = 0$ |).1 03 |
| | 7 | milliferad. | |
| | (b) | Write short note on voltage regulator. | 04 |
| A | (c) | Explain 555 Monostable multivibrator. | 07 |
| 0.5 | (0) | OR Evaluin cample and hold circuit using OP AMP | 03 |
| V.3 | (a) (b) | Explain sample and hold circuit using OP-AMP. Design A-stable multivibrator using IC 555 for Ton = 50% of T, take I | |
| | (0) | 1 KHz, $C = 0.1$ milliferad. | . – 04 |
| 4 | (c) | Explain A-stable multivibrator using IC 555. | 07 |
