Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-IV(NEW) EXAMINATION - WINTER 2022

Subject Code:3141002 Date:13-			2-2022	
Subj	ect]	Name:Analog Circuit Design		
Time:10:30 AM TO 01:00 PM Total Ma			rks:70	
Instru				
	1.	A A		
	2. 3.	Make suitable assumptions wherever necessary. Figures to the right indicate full marks.		
	4.	Simple and non-programmable scientific calculators are allowed.		
			MARKS	
Q.1	(a)	Define following terms.	03	
C	()	CMRR, PSRR, Slew Rate.		
	(b)	Explain Voltage Transfer Curve of OP-AMP.	04	
	(c)	Write and explain differential amplifier using two OP-AMP.	07	
		71	0.0	
Q.2	(a)		03	
	(b)		04 07	
	(c)	OR	07	
	(c)	Explain instrumentation amplifier circuit operation using OP-AMP.	07	
	(-)			
Q.3	(a)		03	
	(b)	1 00	04	
	(c)		07	
0.2	(0)	OR Define following towns	03	
Q.3	(a)	Define following terms. Lock Range for PLL, Capture Range for PLL, Frequency Stability for	03	
		Oscillators.		
	(b)		04	
	(c)		07	
Q.4		Explain floating load V to I converter using OP-AMP.	03	
	`	Explain phase shift oscillator using OP-AMP in detail.	04	
	(c)	Explain CE short-circuit current gain with resistive load R _L . OR	07	
Q.4	(a)		03	
~	(b)		04	
	(c)		07	
Q.5	(a)	Design Monstable multivibrator using timer IC for $T_P = 11$ millisecond,	03	
		take $C = 0.1$ microferad.		
	(b)		04	
	(c)	Explain A-stable multivibrator using timer IC.	07	
0.5	OR OF (a) Explain half ways partifier aircuit vains OR AMP			
Q.5	(a) (b)		03 04	
) (U)	Design A-stable multiviolator using ic 353 for $100 = 75\%$ of 1, take $1 = 1$ KHz, $C = 0.1$ microferad.	U-1	
١	(c)	Explain monostable multivibrator using IC 555.	07	
