

GUJARAT TECHNOLOGICAL UNIVERSITY**BE - SEMESTER- III EXAMINATION – SUMMER 2020****Subject Code: 3130704****Date: 02/11/2020****Subject Name: Digital Fundamentals****Time: 02:30 PM TO 05:00 PM****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

		MARKS
Q.1	(a) Prove following using the Boolean algebraic theorems:	03
	1. $A + A'B + AB' = A + B$	
	2. $AB + A'B + A'B' = A' + B$	
	(b) List out three basic logic operations. Realize these operations using NOR gates only.	04
	(c) Do as directed:	07
	1. Express decimal number 60.875 into binary form.	
	2. One 8421 code word is transmitted in Hamming code with even parity checking. The received word is 0101000. Find out the correct code word and write decimal equivalent.	
Q.2	(a) Minimize following Boolean function using K-map:	03
	$X(A,B,C,D) = \sum m(0, 1, 2, 3, 5, 7, 8, 9, 11, 15)$	
	(b) 1. Convert $Y = AB + AC' + BC$ into canonical SOP form.	04
	2. Convert $Z = (A+B)(A+C)(B+C')$ into canonical POS form.	
	(c) Write a brief note on BCD-to-7-segment decoder/driver. Set up a single 7-segment LED display using 7447 BCD-to-7-segment decoder/driver.	07
	OR	
	(c) Write a brief note on full subtractor with the help of its TT. Also design full subtractor logic circuit using 3 x 8 decoder and OR gates. Use X, Y, & Z as input variables and D & B as output variables.	07
Q.3	(a) Construct D FF using SR FF. Write truth table of D FF.	03
	(b) Draw & explain in brief the logic diagram of 4-bit bidirectional shift register.	04
	(c) Design a 3-bit synchronous up counter using JK flip-flops.	07
	OR	
Q.3	(a) List out various application of the shift register and explain any one.	03
	(b) Construct a 3-bit ripple up counter with preset and clear facility using T FFs.	04
	(c) Write a short note on the applications of the flip-flops.	07
Q.4	(a) List out various methods of simplifying a given Boolean function. Solve $F = AB + AB'$ using any two methods.	03
	(b) Discuss any two characteristics of a D/A converter.	04
	(c) Draw & explain weighted-resistor D/A converter with necessary equations.	07
	OR	
Q.4	(a) Minimize the following logic function using K-map:	03
	$F(A,B,C,D) = \sum m(1, 3, 5, 8, 9, 11, 15) + d(2,13)$	
	(b) Write a brief note on quantization and encoding.	04
	(c) Draw & explain Flash A/D converter with necessary decoding table. Also mention pros & cons of the same.	07
Q.5	(a) Differentiate between RAM & ROM.	03

- (b) How SR FF can be converted into JK FF? Draw & explain in brief. **04**
(c) Write a short note on ROM & its types. **07**

OR

- Q.5** (a) Compare SRAM with DRAM. **03**
(b) Draw & explain the block diagram of ALU. **04**
(c) Draw and explain in brief block diagram of CPLD. Also compare CPLD with FPGA. **07**

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