GUJARAT TECHNOLOGICAL UNIVERSITY			
MIE – SEMIESTER – I (New)– EXAMINATION – WINTER-2019 Subject Code: 2710213			2020
Subjec		ue: 5/10215 Date: 00-01	-2020
Subjec	et Na	me: Distributed Systems	-
Time: 02:30 PM TO 05:00 PM Total Mark			ks: 70
Instruct	ions:	anat all suggions	
-	1. Atto 2. Mal	empt all questions. Re suitable assumptions wherever necessary.	
	3. Fig	ures to the right indicate full marks.	
Q.1	(a)	Explain all forms of transparency parameter in distributed operating	07
	<b>(b)</b>	systems. Briefly explain the concept of stateful and stateless servers with diagram	07
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Q.2	<b>(a)</b>	Explain IPC Synchronization in detail.	07
	(b)	Explain circuit switching and packet switching. Mention the differences	07
		between circuit switching and packet switching. OR	
	<b>(b)</b>	Differentiate between monolithic kernel and microkernel approaches for	07
		designing distributed operating system.	
Q.3	<b>(a)</b>	Explain CORBA Architecture in detail.	07
	<b>(b)</b>	Discuss the main features of ATM technology.	07
		OR	
Q.3	(a)	Which are the different techniques to avoid deadlock in Distributed	07
	( <b>h</b> )	What is a RPC? Explain RPC Execution process in detail with	07
	(0)	diagram.	07
Q.4	<b>(a)</b>	1. What are the differences between RMI and RPC?	04
		2. Explain the significance of wait-for graph with respect to deadlock.	03
	<b>(b)</b>	What is the role of "binding agent" in client server binding? Explain	07
		types of binding in detail.	
04	(a)	What will happen in a bully algorithm for electing a coordinator	07
Q.4	( <b>a</b> )	when two or more process almost simultaneously discover that the	07
		coordinator has crashed? Suggest some suitable mechanism.	
	<b>(b)</b>	Explain name space and name server.	07
<b>Q.5</b>	(a)	Which are the different types of process scheduling techniques? Explain	07
	(h)	Explain the technique to avoid the faults in distributed systems	07
	(0)	OR	07
Q.5	<b>(a)</b>	Explain the causal consistency model for a distributed shared memory	07
		system.	
	<b>(b)</b>	What is thread? Compare user level thread with kernel level thread.	07

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