Seat No.:	Enrolment No.

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

MCA - SEMESTER- II EXAMINATION - WINTER 2018

	•		Date: 05-01-2019	
Ti	-	t Name: Operating Systems 02.30 pm to 5.00 pm Total Marks: 70	)	
1113	1 2	<ol> <li>Attempt all questions.</li> <li>Make suitable assumptions wherever necessary.</li> <li>Figures to the right indicate full marks</li> </ol>		
Q.1	(a)	Define the following: (Any Seven)  1. Monitor  2. Multitasking  3. Interrupts  4. Response time  5. Memory fault  6. Busy waiting or Spin waiting  7. Dispatcher  8. Jacketing	07	
	<b>(b)</b>	Explain Seven-state Process Models mentioning all the transitions.	07	
Q.2	(a) (b)	<ul> <li>i) Explain principles of concurrency with the help of suitable example.</li> <li>ii) Define Thrashing</li> <li>i) Differentiate between Process and Thread.</li> <li>ii) Explain Resource Allocation Graph and its usage with example.</li> </ul>	05 02 04 03	
	<b>(b)</b>	<ul><li>i) What is Deadlock? Discuss the necessary and sufficient conditions for a Deadlock to occur.</li><li>ii) Define semaphore with its' three operations. Define also binary semaphore.</li></ul>	03 04	
Q.3	<ul><li>(a)</li><li>(b)</li></ul>	Define Paging. Explain the logical to physical address translation mechanism in paging with example.  Explain Banker's algorithm with example.	07 07	
Q.3	(a) (b)	<b>OR</b> What is Translation Lookaside Buffer? Explain the working of TLB with flowchart. Explain Dining Philosopher Problem. Give a solution using Monitor.	07 07	
Q.4	(a)	Explain the Readers/Writers problem. Give a solution using semaphore if writers having a priority.	07	
	<b>(b)</b>	Briefly describe the three types of Processor Scheduling.  OR	07	
Q.4	(a) (b)	Total No of pages for the process are 5 and total number of frames allocated to this process are 3 (using Fixed frame allocation).  The page address stream formed by executing the program is as follows: (2 3 2 1 5 2 4 5 3 2 5 2)  Judge which page replacement algorithm among OPT, LRU and FIFO works better. Provide your justification for the same.  Discuss Direct Memory Access (DMA) for performing I/O.	07	
Q.5	(a)	i) Define File Management System.	03	
	<b>(b)</b>	<ul> <li>ii) Explain FIFO and SSTF disk scheduling algorithm.</li> <li>Apply (i) Round Robin with quantum = 4 (ii) First come First serve-FCFS (iii) SPN algorithm for the following set of processes.</li> <li>1. Draw Gantt chart showing execution of these processes.</li> </ul>	04 07	

- 2. Calculate turnaround time for each process and each algorithm.
- 3. Calculate waiting time for each process and each algorithm.
- 4. Calculate finish time for each process and each algorithm.

Process	Arrival Time	Service Time
A	0	3
В	2	6
С	4	4
D	6	5
Е	8	2

OR

Q.5 (a) What is RAID? Explain various levels of RAID.

- **07**
- **(b)** Explain fetch policy and replacement policy in memory management

07

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