GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130004
Semester – III
Subject Name: Effective Technical Communication

Type of course: Communication and ethics

Prerequisite: Zeal to learn the subject

Rationale: The rationale of the curriculum is to help students learn technical communication along with necessary moral and ethical dimensions of engineering.

Teaching and Examination Scheme:

Teaching Scheme			Credits	Examination Marks				Total Marks
				Theory Marks		Practical Marks		
L	T	P	C	ESE (E)	PA (M)	ESE (V)	PA (I)	
2	0	2	3	70	30	30	20	150

Contents:

Sr. No.	Topics	Teaching Hours	Module Weightage
1	**Dynamics of Communication**: Definition and process Kinesics Proxemics Paralinguistic features Importance of Interpersonal and Intercultural Communication in today's organizations	06	20%
2	**Technical Writing**: Report writing Technical proposal Technical description Business letters(sales, order, complaint, adjustment, inquiry, recommendation, appreciation, apology, acknowledgement, cover letter) Agenda of meeting, Minutes of meeting Resume writing	08	25%
3	**Technical Communication**: Public speaking Group discussion Presentation strategies Interview skills Negotiation skills Critical and Creative thinking in communication	06	20%
4	**Ethics in Engineering**: Scope of engineering ethics Accepting and sharing responsibility Responsible professionals and ethical corporations Resolving ethical dilemmas Making moral choices	04	12%
5	**Etiquettes**: Telephone etiquettes Etiquettes for foreign business trips Visits of foreign counterparts Etiquettes for small talks	05	16%

	Respecting privacy Learning to say NO Time management		
6	**Self-development and Assessment:** Change, Grow, Persist, Prioritize, Read, Learn, Listen, Record, Remember, Asses, Think, Communicate, Relate, Dream.	03	7%

Distribution of Theory Marks					
Remember	Understand	Analysis	Application	Evaluation	Creativity
05	**05**	**15**	**15**	**15**	**15**

Language Laboratory Activities:

Sr. No.	**Practical/ Exercise**	**Apprx. Hours required**	**Preferably to be conducted in:**
1	Role Play	02	Classroom/Hall
2	Letter writing: Formal	02	Classroom/Lab
3	Group Discussion	04	Classroom/Hall
4	Presentations	04	Classroom/Hall
5	Book Review(Preferably related to self-development)	04	Classroom/Hall
6	Mock Interview	04	Classroom/Hall
7	Report writing	02	Classroom/Lab
8	Case studies related to unit 4, 5 and 6	06	Classroom/Lab
9	Conducting meetings and minutes of meeting	02	Classroom/Hall
10	Practical assessment	02	Classroom/Lab

Suggested books for review:

1. You Can Win by Shiv Khera
2. How to Win Friends and Influence People by Dale Carnegie
3. Getting Things Done: The Art of Stress Free Productivity by David Allen
4. Quiet: The Power of Introverts in a World That Can't Stop Talking by Susan Cain
5. The Alchemist by Paulo Coelho
6. The 7 Habits of Highly Effective People by Stephen Covey
7. What to Say When You Talk to Yourself by Dr. Shad Helmstetter
8. The Big Leap by Gay Hendricks
9. Thinking Fast and Slow by Daniel Kahneman
10. The Art of Thinking Clearly by Ralf Dobelli
11. Upside Down Key by Sudha Murthy
12. Born to be Happy by Pramod Batra
13. Kiss That Frog by Brian Tracy

14. Build From Scratch by Vineet Bajpai
15. Ten Much by A G Krishnamoorthy
16. Poor Little Rich Slum by Rashmi Bansal
17. Our Ice Berg is Melting by John Paul Cotter
18. Most and More by Mahatria Ra
19. Third Curve by Mansoor Ali Khan
20. Selected Short Stories of Rabindranath Tagore edited by William Radice
21. That Thou Art by Dhruv Bhatt
22. Old Man and the Sea by Ernest Hemingway

Reference Books:

1. Raman and Sharma, *Technical Communications*, OUP, New Delhi, 2017
2. Lata and Kumar, *Communication Skills*, OUP, New Delhi, 2018
3. Mike Martin and Roland Schinzinger, *Ethics in Engineering*, McGraw Hill, New York, 2014
4. Mohapatra and Sreejesh S., *Case Studies in Business Ethics and Corporate Governance*, Pearson, UP, 2013
5. Ramesh and Ramesh, *The Ace of Soft Skills*, Pearson, UP, 2019
6. Sherfield, Montgomery and Moody, *Cornerstone: Developing Soft Skills*, UP, 2009

Open Sources:

https://www.scu.edu/ethics/focus-areas/more/engineering-ethics/engineering-ethics-cases/

Course Outcomes:

At the end of the course students will be able to:

Sr. No.	Course Outcome	Weightage
1	Define and discuss dynamics of Verbal and Non Verbal aspects of Communication	20%
2	Write various formal documents of technical and professional communication	25%
3	Communicate in diverse formal situations taking place in organizations	20%
4	Illustrate and examine the knowledge of ethical aspects of engineering	12%
5	Demonstrate and explain social and professional etiquettes	16%
6	Plan self-development and practice self-assessment	7%

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130007
Semester – III
Subject Name: Indian Constitution

Type of course: Mandatory course

Prerequisite: NA

Rationale: NA.

Teaching and Examination Scheme:

Teaching Scheme			Credits	Examination Marks				Total Marks
				Theory Marks		Practical Marks		
L	T	P	C	ESE (E)	PA (M)	ESE (V)	PA (I)	
2	0	0	0	50	0	0	0	50

Contents:

Sr. No.	Topics	Total Hours
1	Meaning of the constitution law and constitutionalism	01
2	History of Indian Constitution	02
3	Salient features and characteristics of the Constitution of India	01
4	Fundamental rights	02
5	Right to Equality under Article – 14	02
6	Right to certain Freedom under Article 19	02
7	Scope of the Right to Life and Personal Liberty under Article 21	02
8	Fundamental Duties and its legal status	02
9	The Directive Principles of State Policy – Its importance and implementation	02
10	Federal structure and distribution of legislative and financial powers between the Union and the States	03
11	Parliamentary Form of Government in India – The constitution powers and status of the President of India	02
12	Powers and Procedure for Amendments in Indian Constitution	01
13	History of amendments in Indian Constitutional	02
14	Emergency Provisions : National Emergency, President Rule, Financial Emergency	03
15	Local Self Government – Constitutional Scheme in India	03

Course Outcomes:

Sr. No.	CO statement	Marks % weightage
CO-1	Enhance human values , create awareness about law enactment and importance of Consitution	10%
CO-2	To Understand the Fundamental Rights and Fundamental Duties of the Indian Citizen to instill morality, social values, honesty, dignity of life and their social Responsbilities.	30%
CO-3	Create Awareness of their Surroundings, Society, Social problems and their sutaible solutions while keeping rights and duties of the citizen keeping in mind.	20%
CO-4	Understand distribution of powers and functions of Local Self Government.	20%
CO-5	Understand the National Emergency, Financial Emergency and their impact on Economy of the country.	20%

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130007

Reference Books :

1. Constitutional Law of India, Dr. J.N. Pandey, Central Law Agency
2. Introduction to the Consitution of India, Durga Das Basu, LexisNexis.
3. Indian Constitutional Law, M.P. Jain, LexisNexis
4. V.N.Shukla's Constitution of India, Mahndra Pal Singh, Eastern Book Company
5. Constitutional Law – I Structure, Udai Raj Rai, Eastern Book Company

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130008
Semester III
Design Engineering 1 A
Module 1: Understanding Design Thinking

Type of Course: Project Work

Prerequisite: Optimistic mind-set, Enthusiasm of learning new things, Unlearn yourself

Teaching and Examination Scheme:

Teaching Scheme			Credits	Examination Marks				Total Marks
				Theory Marks		Practical Marks		
L	T	P	C	ESE (E)	PA (M)	ESE Viva (V)	PA (I)	
0	0	2	1	0	0	80	20	100

Relevance

This course is meant for beginners. The course is designed to imbibe Design Thinking understanding and mind-set for the 3rd semester students.

Objective: Understanding Design Thinking

The course aims to expose students to the basic process and framework of Design Thinking and relevant tools & techniques for Creativity & Innovation.

Course Contents

This Course is designed to give very basic understanding of the Design Thinking methodology. In DE-1A, student will select very basic and small, individual or team project irrespective of their branch. This project would be from very general topic/domain like designing something for yourself/parents/Teacher/Friends (Whole class may select single project topic or similar topic in different small groups to have healthy competition among the class). This kind of basic project in 3rd semester would help in understanding of Design Thinking process easily when much technicality is not involve. In this module, student will use whole Design Thinking process as shown in fig.1 of general guideline document to complete their projects but here the learning objective or focus would be more on Observation or Empathy process. So students need to give more time to these phases and then reach up to the rough prototype phase. The content is divided into week-wise activities as shown below to better understand the course and to give enough time to all the learning aspects and students need to follow the same but depending upon the type and nature of projects, students and guide may allocate more/less time to the activities.

Design Thinking Process – with Tools & Techniques			
Module 1 (DE-1A): Understanding Design Thinking			
Broad segment	Week	Description	Operational need
Design Thinking Introduction	1	○ Overview, objective and goal of this course ○ What is Design Thinking? - Its importance, socio-economical relevance ○ Design thinking to foster innovation ○ Relevance of design and design thinking in engineering ○ Systematic problem identification & problem solving approaches	○ Brief lecture/exercise ○ Hands on exercise to understand attributes of Design Thinking
	2	○ Domain Selection (general topic/products in 3rd semester) ○ Team Building Exercise ○ Log book, documentation strategy – introduction, importance, preparation	○ Brief lecture/exercise ○ Hands-on sessions with cases/examples ○ Individual logbook is required
	3	○ Learning tools ✓ Design in nature/Bio-mimicry ✓ Design as a System approach ✓ Design as listening tool for mapping users' unmet needs	○ Brief lecture/exercise ○ Next week Students need to present on the learning from these topics
Empathization Phase	4,5,6	○ Observation: Through AEIOU framework ✓ Orientation to Field Work – Need for field visit? ✓ What/How/Where to Observe ✓ Ethnographic tools and its usage ✓ What difference it will make if the problem solved - partially or fully? ✓ Could solution be worse than the problem? ✓ Key pain and pleasure points ✓ Understanding of User Contexts ✓ Log book exercise ✓ Analysis of Data - Mind Mapping ○ Immerse via Role Playing	○ Students will be introduced to different observation/scouting methods in the theory session in class for all four weeks in different sessions ○ Then during weeks, they need to visit their selected domain/place for getting insights and define problems. ○ Minimum 4-5 field trips will be required to get better insights on users' needs.

		○ Interview: ✓ Formal and Informal interview ✓ Students may use Stanford methods given in below link - http://dschool.stanford.edu/wp-content/uploads/2013/10/METHODCARDS-v3-slim.pdf	
		○ Summary of AEIOU activity/inputs ○ Preparation of Mind Map, Empathy Map	○ Class as well as homework/field activity
Define Phase: Problem Definition by secondary research ,group work and presentation	7	○ Secondary research/Prior art search (prior art search is continuous activity and can be used in any phase to strengthen the idea) ○ Group wise presentation followed by Discussion ○ Define Problem statement (format is given in reference PPT on DE portal ○ Verification of problem identified by team through users/stakeholders	○ After rigorous and systematic field exercises, empathization and Secondary Research activities -student teams need to define their problem here (it can be further validate through Ideation phase)
Ideation Phase	8	○ Preparation of Ideation canvas ✓ Brainstorming (What, Why, How, When, For Whom) ✓ Situation/Context/Location ✓ Props/non-living things/tools/equipment ✓ Opportunity mapping	○ 2 hour – explanation of Ideation canvas to class ○ Then students will work on their Ideation canvas ○ Ideation activities shall be performed in class with team members under guidance of teacher
	9	○ Combination of Ideas from Ideation canvas ○ Sketching of mock concepts in log book ○ Design Thinking is a Convergent-Divergent process	○ Student teams need to discuss their Ideation canvas with other teams, faculty guides and users and take feedbacks
	10	○ Prioritizing and finalizing Idea (After group discussion and consulting with faculty guide, student teams need to select their final problem & idea for further development)	○ Students team need to validate the final Problem & idea/concept with Users/Stakeholders after this activity

Product Development Phase	11	○ Preparation of Product Development Canvas (PDC) ✓ Product Experience ✓ Product Functions ✓ Product Features ✓ Components ○ Discussion on Product Development Canvas (PDC)	○ 2 hour – explanation of product development canvas to class ○ Then students will work on their PD canvas (min 3 hour continuous workshop) ○ Till 12th week of the course, Students team will discuss on their PDC with other groups and guide ○ Refinement of PDC after discussion
	12	○ Customer/User Revalidation (Reject/Redesign/Retain) ○ Refinement	○ Till 13th week of the course, student team will consult the Users/Stakeholders for their inputs for concept finalization after various stages and incorporate necessary changes.
Proof of Concept	13	○ Rough Prototype ○ Here strategy is "to fail fast to succeed faster"	○ Very early & rough prototype ○ Made up of paper, cardboard, thermocol etc. whichever material is available
Feedback & Final Report	14	○ Upload duly signed Continuous Assessment Card ○ Feedback, Online certificate generation through DE portal ○ Final Report	○ As per the feedback received from Users/Stakeholders/other student groups/guide, student teams need to modify their design and further action plan. ○ Report writing should be continuous activity throughout the semester

Submissions by the end of 3rd semester shall be:

A. Process Report comprising:
   a. Introduction (Describe your project in detail including domain – type, place, why and how team selected this domain and why this domain is important in relation to Design Thinking/Human-Centered process etc.)
   b. Preparation of canvases based on different phase of Design Thinking
   c. Feedback analysis with the user shall be clearly included in the report
   d. Summary of findings of Prior Art Search on purpose/project theme (2 summary papers per student)
   e. Summary of the learning from Design Thinking
   f. Summary on validation process and refinement in the rough prototype
   g. Any other important aspects you feel should be included

B. AEIOU framework
C. Mind Map
D. Empathy Map
E. Ideation Canvas
F. Product Development Canvas (PDC)
G. Rough prototype model/Conceptual Plan-Layout for process related branches
H. Individual Log Book (duly signed by faculty guide)
I. Continuous Assessment Card for Internal Evaluation (Document separately available on GTU website)

Note: As per the guidelines and evaluation schemes given in this document, students need to prepare report for their projects. Separate report format will not be provided by University, students and faculty members may create their own creative formats. However, in general guidelines document uploaded on GTU website, there are some report format links are given which may help for report format.

To,

The Principals/Directors of Colleges/Institutes, the Heads of Departments and GTU/Design Engineering coordinators:

Students deserve a proper practical/ viva/project examination of the work that they have done over the semester (or over the year for a 2-semester project). It is the responsibility of the University and Colleges that all its examinations are conducted fairly, sincerely and with due diligence. So please look into the following:

1. Please make proper arrangements so that all the examinations start in-time. If due to any reason, the exam should not start at the scheduled time, please inform the examiners that they should take extra time. But in no case the viva/ practical exam be conducted in a hurry without giving sufficient time for evaluation of every student. If an exam is scheduled to be held over two days, please make the necessary arrangements.

2. The University expects the Deans (and or special teams headed by the Dean or his/ her nominee) to visit the Colleges during the practical/ viva examinations. As it came to University's notice that some examiners and colleges are completing viva exam in 1 or 2 hours' time of entire class which is not acceptable in any case and it's immoral practice for any education institute. So all stakeholders need to take extra care of this issue.

3. Please see that all the necessary help and information is provided to examiner. Please receive them so that they can do their job properly without wasting their time in searching for the place and in contacting the concerned departments and students. If they wish to visit the laboratories/workshops, please make the necessary arrangements.

4. Please inform the examiner that he/she must note down the best 3 projects of the department and convey the details of such projects by uploading the details of the project or/and the complete project report on the University's server or send it to design@gtu.edu.in .

5. In case Internet or the server should not work, please provide the technical help to the examiner for preparing a CD of the reports of the best three projects of every department and please make arrangements to deliver the CD to the examination/BE section of the University.

PROCESS OF EVALUATION: At the ensuing 3rd semester examinations, the work of the students in Design Engineering – 1A is to be evaluated through Internal Viva exam and the evaluation is to be out of 80 marks. Institute may organize inter-department viva or project show case so students would get various expert opinions to motivate them.

For 3rd semester, internal Viva-Voce examination will be conducted at the end of the semester by a team of three examiners - One internal guide, one inter/own departmental faculty, one industry expert (industry expert may be optional but recommended). Internal examiners/teachers must be trained in Design Thinking through the FDP conducted by University.

EVALUATION SCHEME:

Sr. No.	Particular	Sub-Head Weightage
1.	Understanding of Design Thinking methodology/ need ✓ Importance and understanding of Design Thinking for innovation, entrepreneurship, societal solutions with various learning tools	15
2.	Observation towards Empathy ✓ Field Activity/observation and outcome ✓ Mind Mapping - Summarization and data analysis ✓ Observation Technique (AEIOU Framework)	20
3.	Log book (Individual completed log book, duly signed by guide regularly) Continuous Assessment Card for Internal Evaluation (Complete and duly signed by guide regularly)	10
4.	Understanding of Canvases/Framework ✓ AEIOU, Mind Mapping ✓ Empathy mapping ✓ Ideation Canvas ✓ Product development Canvas	15
5.	Design Problem Definition ✓ Prior art search/Secondary research ✓ Diachronic and Synchronic analysis	10
6.	Report: Compilation of work report (process report), Online Certificate generated through DE Portal, Future action plan, Question and Answer, Communication Skill, Attitude	10
		80

Note:

- ✓ Total Marks for the subject: 100 (Internal end semester viva exam – 80 & Internal continuous evaluation – 20)
- ✓ Minimum passing marks: 40/80
- ✓ Examiner essentially needs to evaluate the learning process of the student during the semester, not only the final outcome. As outcome is important for any project but during the student stage, projects are intended for practical learning and "Learning by doing" is the Mantra for Design Engineering subject (One should celebrate the failure also and learn from it to get success). So

please evaluate the Design Thinking process and their learning properly with giving sufficient time for each project.

- ✓ Students need to explain all canvases prepared in hard copy to the panel of examiners.
- ✓ Power point presentation is not mandatory.

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130905
Semester – III
Subject Name: Control System Theory

Type of course:

Prerequisite:

Rationale: Automatic control of industrial processes is essential for increasing the output and in turn the profit of an industry. As a result, most of the companies are using automatic control of the machineries and processes. As an engineer, a student must know the basics of automatic control system. This subject is intended to supplement the basic skill of an engineer.

Teaching and Examination Scheme:

Teaching Scheme			Credits	Examination Marks				Total Marks
				Theory Marks		Practical Marks		
L	T	P	C	ESE (E)	PA (M)	ESE (V)	PA (I)	
3	0	2	4	70	30	30	20	150

Content:

Sr. No.	Content	Total Hrs	% Weightage
1	**Introduction to control problem** Industrial Control examples. Mathematical models of physical systems. Control hardware and their models. Transfer function models of linear time-invariant systems. Feedback Control: Open-Loop and Closed-loop systems. Benefits of Feedback. Block diagram algebra.	**05**	**15**
2	**Time Response Analysis** Standard test signals. Time response of first and second order systems for standard test inputs. Application of initial and final value theorem. Design specifications for second-order systems based on the time-response. Concept of Stability. Routh-Hurwitz Criteria. Relative Stability analysis. Root-Locus technique. Construction of Root-loci.	**12**	**25**
3	**Frequency-response analysis** Relationship between time and frequency response, Polar plots, Bode plots. Nyquist stability criterion. Relative stability using Nyquist criterion – gain and phase margin. Closed-loop frequency response.	**08**	**20**
4	**Introduction to Controller Design** Stability, steady-state accuracy, transient accuracy, disturbance rejection, insensitivity and robustness of control systems. Root-loci method of feedback controller design. Design specifications in frequency-domain. Frequency-domain methods of design. Application of Proportional, Integral and Derivative Controllers, Lead and Lag compensation in designs. Analog and Digital implementation of controllers.	**12**	**25**
5	**State variable Analysis** Concepts of state variables. State space model. Diagonalization of State	**06**	**15**

	Matrix. Solution of state equations. Eigen values and Stability Analysis. Concept of controllability and observability. Pole-placement by state feedback. Discrete-time systems. Difference Equations. State-space models of linear discrete-time systems. Stability of linear discrete-time systems.		

Suggested Specification table with Marks (Theory):

Distribution of Theory Marks					
R Level	U Level	A Level	N Level	E Level	C Level
20	**30**	**20**	**20**	**10**	**-**

Legends: R: Remembrance; U: Understanding; A: Application, N: Analyze and E: Evaluate C: Create and above Levels (Revised Bloom's Taxonomy)

Note: This specification table shall be treated as a general guideline for students and teachers. The actual distribution of marks in the question paper may vary slightly from above table.

Reference Books:

- M. Gopal, "Control Systems: Principles and Design", McGraw Hill Education, 1997.
- B. C. Kuo, "Automatic Control System", Prentice Hall, 1995.
- K. Ogata, "Modern Control Engineering", Prentice Hall, 1991.
- J. Nagrath and M. Gopal, "Control Systems Engineering", New Age International, 2009

Course Outcomes:

Sr. No.	CO statement	Marks % weightage
CO-1	Understand the fundamental of feedback control system.	15
CO-2	Understand time response specifications and determine the (absolute) stability of a closed-loop control system	25
CO-3	Determine the time and frequency-domain responses of first and second-order systems to step and other standard inputs.	25
CO-4	Design controller as per given specifications using different techniques	20
CO-5	Express and solve system equations in state-variable form (state variable models).	15

List of Open Source Software/learning website:

- E-materials available at the website of NPTEL- http://nptel.ac.in/

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130906
Semester – III
Subject Name: Electrical Circuit Analysis

Type of course:

Prerequisite:

Rationale: Electrical circuits are the integral elements of the power system. Analysis of response of electrical circuits for various inputs is the basic requirement to understand the behavior of the system. The responses for various inputs are in turn helpful to design, implement, operate and control a network effectively. This subject is intended to provide the basic insight into the theory and problems related to electrical circuit analysis.

Teaching and Examination Scheme:

Teaching Scheme			Credits	Examination Marks				Total Marks
				Theory Marks		Practical Marks		
L	T	P	C	ESE (E)	PA (M)	ESE Viva (V)	PA (I)	
3	1	2	5	70	30	30	20	150

Content:

Sr. No.	Content	Total Hrs	% Weightage
1	**Network Theorems** Superposition theorem, Thevenin theorem, Norton theorem, Maximum power transfer theorem, Reciprocity theorem, Compensation theorem. Analysis with dependent current and voltage sources. Node and Mesh Analysis. Concept of duality and dual networks.	10	20
2	**Solution of First and Second order networks** Solution of first and second order differential equations for Series and parallel R-L, R-C, RLC circuits, initial and final conditions in network elements, forced and free response, time constants, steady state and transient state response.	08	20
3	**Sinusoidal steady state analysis** Representation of sine function as rotating phasor, phasor diagrams, impedances and admittances, AC circuit analysis, effective or RMS values, average power and complex power. Three-phase circuits. Mutual coupled circuits, Dot Convention in coupled circuits, Ideal Transformer.	08	20
4	**Electrical Circuit Analysis Using Laplace Transforms** Review of Laplace Transform, Analysis of electrical circuits using Laplace Transform for standard inputs, convolution integral, inverse Laplace transform, transformed network with initial conditions. Transfer function representation. Poles and Zeros. Frequency response (magnitude and phase plots), series and parallel resonances	08	20
5	**Two Port Network and Network Functions** Two Port Networks, terminal pairs, relationship of two port variables, impedance parameters, admittance parameters, transmission parameters and hybrid parameters, interconnections of two port networks.	08	20

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering

Subject Code: 3130906

Suggested Specification table with Marks (Theory): (For BE only)

Distribution of Theory Marks					
R Level	U Level	A Level	N Level	E Level	C Level
15	**30**	**30**	**15**	**10**	**-**

Legends: R: Remembrance; U: Understanding; A: Application, N: Analyze and E: Evaluate C: Create and above Levels (Revised Bloom's Taxonomy)

Note: This specification table shall be treated as a general guideline for students and teachers. The actual distribution of marks in the question paper may vary slightly from above table.

Reference Books:

- M. E. Van Valkenburg, "Network Analysis", Prentice Hall, 2006.
- A. A. Nimje and D. P. Kothari, "Electrical Circuit Analysis and synthesis", New Age International Publications, 2017
- K.S.Suresh Kumar, "Electric Circuit Analysis", Pearson Publications, 2013.
- D. Roy Choudhury, "Networks and Systems", New Age International Publications, 1998.
- W. H. Hayt and J. E. Kemmerly, "Engineering Circuit Analysis", McGraw Hill Education, 2013.
- C. K. Alexander and M. N. O. Sadiku, "Electric Circuits", McGraw Hill Education, 2004.
- K. V. V. Murthy and M. S. Kamath, "Basic Circuit Analysis", Jaico Publishers, 1999.

Course Outcomes:

Sr. No.	CO statement	Marks % weightage
CO-1	Apply the knowledge of basic circuital law and simplify the network using reduction techniques	20
CO-2	Analyze the circuit using Kirchhoff's law and Network simplification theorems	20
CO-3	Infer and evaluate transient response, Steady state response, network functions	25
CO-4	Obtain the maximum power transfer to the load , and Analyze the series resonant and parallel resonant circuit	20
CO-5	Evaluate two-port network parameters.	15

List of Experiments :

This is a suggestive list only:

(1) To verify the Superposition theorem.
(2) To verify the Thevenin and Norton's theorems.
(3) To verify the maximum power transfer theorem.
(4) To verify the reciprocity theorem.
(5) To measure and verify the steady-state and transient time-response of R-L circuit.
(6) To measure and verify the steady-state and transient time-response of R-C circuit.

(7) To measure and verify the steady-state and transient time-response of R-L-C circuit.
(8) To verify the current and voltage phasors in complex AC circuits by measurement and theoretical analysis.
(9) To obtain the solution of first order and second order linear differential equations with Laplace transform.
(10) To obtain the solution of R-L-C networks with impedance functions.
(11) To verify the impedance parameters for a two port network.
(12) To verify the admittance parameters for a two port network.
(13) To verify the hybrid parameters for a two port network.
(14) To verify the transmission parameters for a two port network.

Major Equipment:

List of Open Source Software/learning website:

- E-materials available at the website of NPTEL- http://nptel.ac.in/

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130907
Semester – III
Subject Name: Analog and Digital Electronics

Type of course:

Prerequisite:

Rationale:

Teaching and Examination Scheme:

Teaching Scheme			Credits	Examination Marks				Total Marks
L	T	P	C	Theory Marks		Practical Marks		
				ESE (E)	PA (M)	ESE (V)	PA (I)	
4	0	2	5	70	30	30	20	150

Content:

Sr. No.	Content	Total Hrs
1	**Differential, multi-stage and operational amplifiers** Differential amplifier; power amplifier; direct coupled multi-stage amplifier; internal structure of an operational amplifier, ideal op-amp, non-idealities in an op-amp (Output offset voltage, input bias current, input offset current, slew rate, gain bandwidth product)	**10**
2	**Linear applications of op-amp** Idealized analysis of op-amp circuits. Inverting and non-inverting amplifier, differential amplifier, instrumentation amplifier, integrator, active filter, P, PI and PID controllers and lead/lag compensator using an op-amp, voltage regulator, oscillators (Wein bridge and phase shift). Analog to Digital Conversion.	**10**
3	**Nonlinear applications of op-amp** Hysteretic Comparator, Zero Crossing Detector, Square-wave and triangular-wave generators. Precision rectifier, peak detector.	**8**
4	**Combinational Digital Circuits** Standard representation for logic functions, K-map representation, simplification of logic functions using K-map, minimization of logical functions. Don't care conditions, Multiplexer, De-Multiplexer/Decoders, Adders, Subtractors, BCD arithmetic, carry look ahead adder, serial adder, ALU, elementary ALU design, popular MSI chips, digital comparator, parity checker/generator, code converters, priority encoders, decoders/drivers for display devices, Q-M method of function realization	**10**
5	**Sequential circuits and systems** A 1-bit memory, the circuit properties of Bi-stable latch, the clocked SR flip flop, J- K-T and D types flip-flops, applications of flip-flops, shift registers, applications of shift registers, serial to parallel converter, parallel to serial converter, ring counter, sequence generator, ripple(Asynchronous) counters, synchronous counters, counters design using	**10**

	flip flops, special counter IC's, asynchronous sequential counters, applications of counters.	
6	**A/D and D/A Converters** Digital to analog converters: weighted resistor/converter, R-2R Ladder D/A converter, specifications for D/A converters, examples of D/A converter lCs, sample and hold circuit, analog to digital converters: quantization and encoding, parallel comparator A/D converter, successive approximation A/D converter, counting A/D converter, dual slope A/D converter, A/D converter using voltage to frequency and voltage to time conversion, specifications of A/D converters, example of A/D converter ICs	**8**

Suggested Specification table with Marks (Theory): (For BE only)

Distribution of Theory Marks					
R Level	U Level	A Level	N Level	E Level	C Level
40	**40**	**10**	**10**	**00**	**00**

Legends: R: Remembrance; U: Understanding; A: Application, N: Analyze and E: Evaluate C: Create and above Levels (Revised Bloom's Taxonomy)

Note: This specification table shall be treated as a general guideline for students and teachers. The actual distribution of marks in the question paper may vary slightly from above table.

Reference Books:

1. A. S. Sedra and K. C. Smith, "Microelectronic Circuits", New York, Oxford University Press, 1998.
2. J. V. Wait, L. P. Huelsman and G. A. Korn, "Introduction to Operational Amplifier theory and applications", McGraw Hill U. S., 1992.
3. J. Millman and A. Grabel, "Microelectronics", McGraw Hill Education, 1988.
4. P. Horowitz and W. Hill, "The Art of Electronics", Cambridge University Press, 1989.
5. P.R. Gray, R.G. Meyer and S. Lewis, "Analysis and Design of Analog Integrated Circuits", John Wiley & Sons, 2001.
6. Ramakant A Gayakwad, Op- Amps and Linear Integrated Circuits, Prentice Hall of India
7. R. P. Jain, "Modern Digital Electronics", McGraw Hill Education, 2009.
8. M. M. Mano, "Digital logic and Computer design", Pearson Education India, 2016.
9. A. Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, 2016.

Course Outcomes:

Sr. No.	**CO statement**	**Marks % weightage**
CO-1	Describe the functioning and selection of OP-AMP as per application.	25

CO-2	Design and testing of OP-AMP based circuits.	25
CO-3	Design and implement Combinational and Sequential logic circuits.	25
CO-4	Describe the process of Analog to Digital conversion and Digital to Analog conversion.	25

List of Experiments:

1. Study the different parameter of op-amp.
2. Frequency response of inverting amplifier and non-inverting amplifier.
3. Study of op-amp as inverting amplifier and non-inverting amplifier.
4. OPAMP circuits –integrator, differentiator, and comparator.
5. Phase shift and Wein's Bridge oscillator with amplitude stabilization using OPAMPs.
6. Waveform generation – Square, triangular and saw tooth wave form generation using OPAMPs.
7. Application of op-amp as low pass filter, high pass filter and band-pass filter.
8. Verification of function of Half/Full adder circuits.
9. Verification of function of Binary to Grey code conversion.
10. Verification of function of Latch and flip-flop.
11 Verification of counter circuit like binary up/down counter, decimal counter, ring counter, Johnson counter etc.
12. Verification of Specification and Performance indices of D/A and A/D converters

Major Equipment:

- ✓ Trainer kits related to Analog and Digital electronics.

List of Open Source Software/learning website:

1. Courses available through NPTEL.
   - website : nptel.ac.in

GUJARAT TECHNOLOGICAL UNIVERSITY

Bachelor of Engineering
Subject Code: 3130908
Semester – III
Subject Name: Applied Mathematics for Electrical Engineering

Type of course: Basic Science Course

Prerequisite: The students are required to have a reasonable understanding ofCalculus, Differential equations and Linear algebra and introductory knowledge of probability and statistics.

Rationale:
There is different kind of systems which requires through mathematical analysis tools. The type of systems which requires such tools are linear systems, nonlinear systems and dynamical systems. Apart from them, it requires systematic study of uncertainty (randomness) by probability - statistics and curve fitting. The different methods like numerical methods are required to be studied.

Teaching and Examination Scheme:

Teaching Scheme			Credits	Examination Marks				Total Marks
L	T	P	C	Theory Marks		Practical Marks		
				ESE (E)	PA (M)	ESE (V)	PA (I)	
3	2	0	5	70	30	0	0	100

Content:

Sr. No.	Content	Total Hrs	% Weightage
01	**Numerical Solutions:** Roots of Algebraic and Transcendental Equations : Bisection, false position, Secant and Newton-Raphson methods, Fixed Point Iteration, Rate of convergence,Applications to electrical engineering problems.	06	14 %
02	**Interpolation:** Finite Differences, Forward, Backward and Central operators, Interpolation by polynomials: Newton's forward ,Backward interpolation formulae, Newton's divided formulae and Lagrange's interpolation formulae for unequal intervals, Applications to electrical engineering problems.	06	14 %
03	**Numerical Integration:** Newton-Cotes formula, Trapezoidal and Simpson's formulae, error formulae, Gaussian quadrature formulae, Applications to electrical engineering problems	04	10%
04	**Numerical solution of Ordinary Differential Equations:** Picard, Taylor, Euler methods and Runge-Kutta methods, Applications to electrical engineering problems	04	10%
05	**Curve fitting by the numerical method:** Curve fitting by of method of least squares, fitting of straight lines, second degree parabola and more general curves.	04	10%
06	**Basic Probability:** Experiment, definition of probability,conditional probability, independent events, Bayes' rule, Bernoulli trials, Random variables, discrete random variable, probability mass function, continuous random variable, probability density function, cumulative distribution function, properties of cumulative distribution function, Applications to electrical engineering problems.	10	22 %

07	**Basic Statistics:** Measure of central tendency: Moments, Expectation, dispersion, skewness, kurtosis, Bounds on probability, Chebyshev's Inequality, Applications to electrical engineering problems.	08	20%

Suggested Specification table with Marks (Theory):

Distribution of Theory Marks					
R Level	U Level	A Level	N Level	E Level	C Level
15	**35**	**20**	**0**	**0**	**0**

Legends: R: Remembrance; U: Understanding; A: Application, N: Analyze and E: Evaluate C: Create and above Levels (Revised Bloom's Taxonomy)

Note: This specification table shall be treated as a general guideline for students and teachers. The actual distribution of marks in the question paper may vary slightly from above table.

Reference Books:

(1) E. Kreyszig, Advanced Engineering Mathematics, John Wiley (1999)
(2) J. L. Devore, Probability and Statistics for Engineering and the Sciences, Cengage Learning
(3) Chapra S.C, Canale, R P, Numerical Methods for Engineers , Tata McGraw Hill, 2003.
(4) Gerald C. F. and Wheatley P.O. , Applied Numerical Analysis (5th Edition), Addison-Wesley, Singapore, 1998
(5) P. G. Hoel, S. C. Port and C. J. Stone, Introduction to Probability Theory, Universal Book Stall.
(6) S. Ross, A First Course in Probability, 6th Ed., Pearson Education India.
(7) W. Feller, An Introduction to Probability Theory and its Applications, Vol. 1, Wiley.
(8) D. C. Montgomery and G. C. Runger, Applied Statistics and Probability for Engineers, Wiley.

Course Outcomes:

After learning the course, the students should be able to:

Sr. No.	**CO statement**	**Marks % weightage**
CO-1	solve algebraic equation related to electric engineering problem by using numerical methods and understand convergent of it	
CO-2	find unknown value of given data by using various interpolation methods and curve fitting	
CO-3	calculate integration and solve differential equations by using numerical methods	
CO-4	understand the terminologies of basic probability and their probability functions and apply it in electrical problems	
CO-5	understand the central tendency methods and apply it in electrical problems	

List of Open Source Software/learning website:

- E-materials available at the website of NPTEL- http://nptel.ac.in/